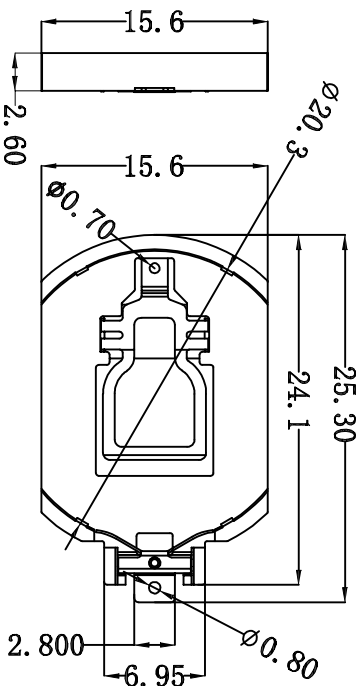


PCB Layout Diagram

Top View

3	TERMINAL (-)	1	PHOSPHOR BRONZE, T=0.20MM	Gold Plating and Ni Under-Plating
2	TERMINAL (+)	1	PHOSPHOR BRONZE, T=0.15MM	Gold Plating and Ni Under-Plating
1	HOUSING	1	PPA	BROWN
NO.	PART NAME	Q'TY	MATERIAL	PLATING & COLOR
	A		NEW	
ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.

APPROVALS		DATE	GEPU 歌普	
DRAWN	ZHIWEI.CHEN	2015.07.07		
CHECKED				
APPROVALS			TITLE:	电池座 SMT
TOLERANCES ARE		30~0.1	±0.30	PART NO.
		10~0.30	±0.20	GP07-CR2016--01
		5~0.10	±0.15	SCALE: 1:1
		~ 5	±0.10	PROJ.
			±2'	DRAWING NO.
				SHEET 1 OF 1

NOTE:

1. MATERIAL:

a. HOUSING:PPA

b. CONTACT:PHOSPHOR BRONZE

(SURFACE PLATING: Au*U")

SEE ORDERING INFORMATION

2. ELECTRIC:

a. CONTACT RESISTANCE: 30 MILLIOHMS MAX

b. INSULATION RESISTANCE: 1000 MEGA OHMS MIN

c. DIELECTRIC VOLTAGE: 500V AC ONE SEC LEVEL

d. OPERATING TEMPERATURE: -25° C ~ +85° C